

Notice of References Cited	Application/Control No. 09/886,855	Applicant(s)/Patent Under Reexamination PITSIANIS ET AL.	
	Examiner Satish S. Rampuria	Art Unit 2124	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,930,508 A	07-1999	Faraboschi et al.	717/158
	B	US-6,305,014 B1	10-2001	Roediger et al.	717/154
	C	US-6,675,380 B1	01-2004	McKinsey et al.	717/161
	D	US-6,507,947 B1	01-2003	Schreiber et al.	717/160
	E	US-6,735,690 B1	05-2004	Barry et al.	712/244
	F	US-2002/0112228 A1	08-2002	Granston et al.	717/155
	G	US-5,442,790 A	08-1995	Nosenchuck, Daniel M.	717/155
	H	US-6,044,450 A	03-2000	Tsushima et al.	712/24
	I	US-6,647,547 B1	11-2003	Kanamaru et al.	717/151
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Diwan et al., Memory system performance of programs with intensive heap allocation, August 1995, ACM Transactions on Computer Systems (TOCS), Volume 13 Issue 3, Pages 1-30
	V	Marcelo et al., Cache performance of fast-allocating programs, October 1995, Proceedings of the seventh international conference on Functional programming languages and computer architecture, Pages 1-13
	W	Suhyun et al., Unroll-based register coalescing, May 2000, Proceedings of the 14th international conference on Supercomputing, Pages 1-10
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.